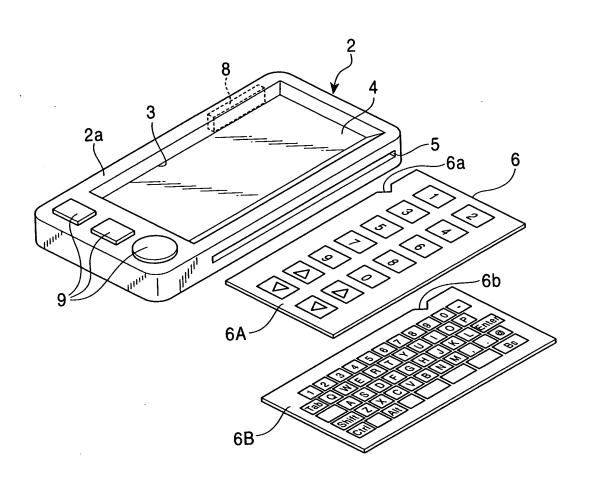
1/4

FIG. 1



∞ MEMORY DETERMI-NATION MEANS INPUT MEANS CONTROL DISPLAY MEANS TRANSMISSION SECTION RECEIVING SECTION TRANSMISSION SECTION RECEIVING SECTION 33 DISPLAY SECTION MEMORY 24 CPU 21~

FIG. 3

